

**AMENDMENTS TO THE ABSTRACT**

On page 1, please replace the whole paragraph beginning on line 4 as following:

- 5       A dynamic random access memory (DRAM) is formed on a semiconductor wafer including a substrate, a thin film layer positioned on the substrate, and a photoresist layer positioned on the thin film layer. A first exposure process is performed to form first exposure regions including a plurality of lines  
10       parallel to each other and covering each storage node. A second exposure process is performed to form second exposure regions cutting the plurality of lines of the first exposure regions. The first exposure regions and the second exposure regions of the photoresist layer are then removed for forming an array  
15       photoresist layer on the thin film layer. The array photoresist layer functions as a mask to perform an etching process to the thin film layer for forming an array thin film layer as a storage nodes in the DRAM.

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